

UNIVERSITY COLLEGE OF ENGINEERING

OSMANIA UNIVERSITY, HYDERABAD

M.E. (E.C.E) II - Semester (Regular & PTPG) and IV-Semester (PTPG) Main Examination August 2011

EXAMINATION TIME TABLE

Time : 2.00 PM to 5.00 PM

<p align="center">Examination Centre: Department of ECE, UCE (A), O.U.</p>

Date and Day	Digital Systems			Systems & Signal Processing			Microwave & Radar Engineering	Embedded Systems and VLSI Design
	Regular II-Semester	PTPG II-Semester	PTPG IV-Semester	Regular II-Semester	PTPG II-Semester	PTPG IV-Semester	Regular II-Semester	Regular II-Semester
16-08-2011 TUESDAY	VLSI Physical Design	=====	VLSI Physical Design	Wireless Mobile Communication Systems	=====	=====	Wireless Mobile Communication Systems	VLSI Physical Design
18-08-2011 THURSDAY	Data and Computer Communication Networks	Data and Computer Communication Networks	=====	Digital Spectral Analysis	Digital Spectral Analysis	=====	Microwave Antennas	=====
20-08-2011 SATURDAY	Digital Design & PLD's	Digital Design & PLD's	=====	Speech Signal Processing	=====	Global Navigational Satellite Systems	Microwave Integrated Circuits	Microwave Integrated Circuits
22-08-2011 MONDAY	Real Time Signal Processing	=====	Advanced Computer Organization	Real Time Signal Processing	Real Time Signal Processing	Advanced Computer Organization	Radio Navigational Aids	CPLD & FPGA Architecture & Applications
24-08-2011 WEDNESDAY	Micro Controllers for Embedded System Design	Micro Controllers for Embedded System Design	=====	Adaptive Signal Processing	Adaptive Signal Processing	Optimization Techniques	Numerical Methods in Electromagnetics	Real Time Operating Systems
26-08-2011 FRIDAY	Design for Testability	=====	Design for Testability	Computer Vision	=====	=====	Computer Vision	Design for Testability
29-08-2011 MONDAY	=====	=====	=====	=====	=====	=====	=====	Analog & Mixed Signal IC Design

Note: This Time-Table is also applicable for Pre-Ph.D. students registered for August 2011 Examination.